What's the Problem?

Address Data Centers Challenges with the aid of FPGAs

Performance (latency)   *Energy Consumption*

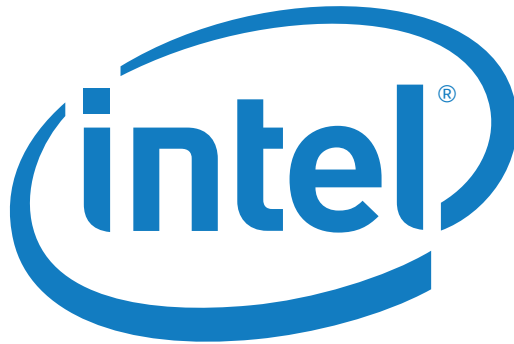
Marco Forconesi   Research Assistant

NaaS Project   University of Cambridge

Why do People Care?

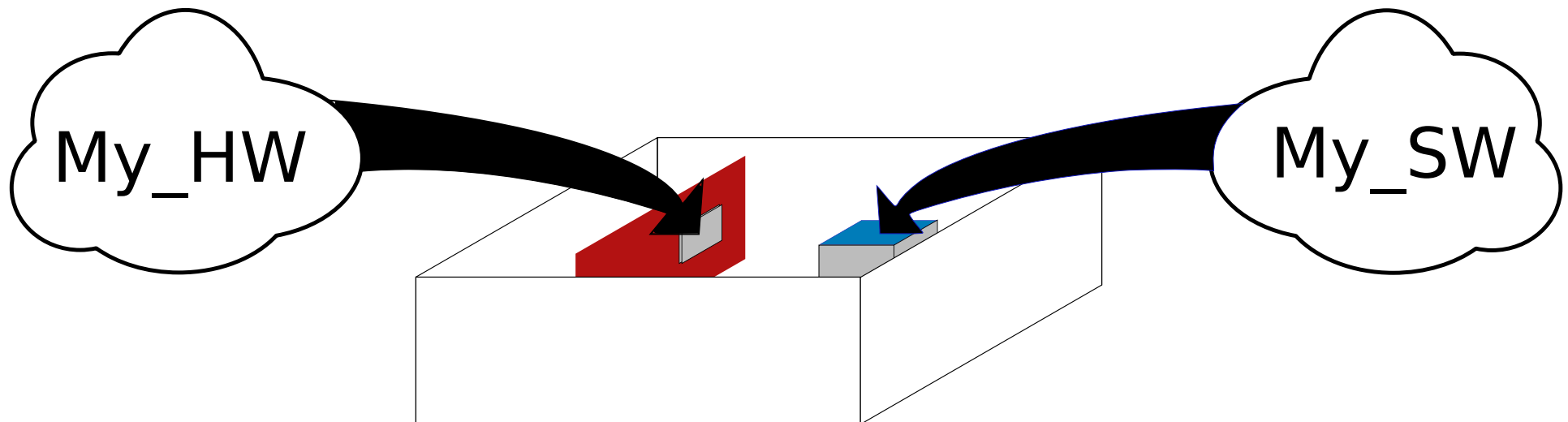| NaaS Project 2014 | Intel's Xeon + FPGA 2014 | Microsoft Bing (Catapult) 2014 |

Why is it Good?

Having the possibility to design
the SW in conjunction with the HW
renders a more efficient solution

My_HW

My_SW

Challenges and Future

Verilog (HDL)
coding is difficult → High Level Languages (HLL)
for HW generation